Measurement Uncertainty Propagation in Transistor Model Parameters via Polynomial Chaos Expansion

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Abstract— We present an analysis of the propagation of measurement uncertainty in microwave transistor nonlinear models. As a case study, we focus on residual calibration uncertainty and its effect on modelled nonlinear capacitances extracted from small-signal microwave measurements. We evaluate the uncertainty by means of the Polynomial Chaos Expansion (PCE) method and compare the results with the NIST Microwave Uncertainty Framework, which enables both sensitivity and Monte-Carlo (MC) analyses for uncertainty quantification in microwave measurements. We demonstrate that, for the considered application, PCE provides results in agreement with classical MC simulations but with a significant reduction of the computational effort.

Index Terms— Microwave measurements uncertainty, FET, nonlinear modeling, polynomial chaos expansion.

I. INTRODUCTION

IN the last decade the study of uncertainty in measurement based microwave transistor nonlinear models has increased significantly. In general, measurements are affected by systematic errors, random errors, and drift errors due to the instrumentation non-idealities. For vector network analyzer (VNA) measurements, these errors cause mismatch, finite directivity and non-ideal frequency response. Hence, calibration is a crucial step in microwave measurement systems, because it allows one to correct for most of the systematic effects introduced by the measurement system itself. Calibration procedures require the measurements of well-known standards. However, these standards are not perfect due to, for instance, mechanical tolerances in the fabrication process. These imperfections add uncertainty in the calibration and, consequently, in the device-under-test (DUT) measurements. In this work, we evaluate how residual

uncertainties in calibration propagate to the parameters of a field-effect transistor (FET) model extracted from multi-bias scattering (S-) parameters. We performed uncertainty analysis with the PCE approach [1], [2]. Next, we compared the results obtained by the PCE method with those obtained by the NIST Microwave Uncertainty framework (MUF) [3]. The NIST MUF is a tool for uncertainty quantification in microwave measurements, which enables both sensitivity analysis and Monte-Carlo simulations. In our analysis, we included uncertainties in the standards of the thru-reflect-line (TRL) calibration. These uncertainties were provided by the foundry and refer to tolerances in the fabrication process of the lines. In Table I we report the nominal value and uncertainty of the used transmission lines. Next, we used the corrected transistor measurements to extract the parameters of a FET model and propagated uncertainty through the extraction procedure, as illustrated in Fig. 1. Finally, we propagated uncertainty in the model parameters to the simulated temporal voltages and currents of the transistor.

We show that, for the considered application, PCE approach provides results comparable to classical MC simulations but with reduced computational effort.

II. UNCERTAINTY QUANTIFICATION

In this Section, we describe the procedure for the extraction of the parameters of the capacitive part of the FET model. The resistive part of the FET model, i.e., the drain-source current generator and the gate Schottky junction model, are replaced with Look- Up-Tables (LUT) derived directly from DC measurements and are assumed to be without uncertainty.

A. Identification of the capacitive model

The device under test is a gallium-arsenide (GaAs) pHEMT (0.15 μm × 200 μm). The extraction of the capacitive core is based on multi-bias S-parameter measurements at $f_0 = 5$ GHz and over the bias range: $0 \text{ V} < V_{ds} < 9$ V and $-1.5 \text{ V} < V_{gs} < 0$ V. We adopted Angelov model [4] in order to describe C_{gs} (1) and C_{gd} (2) nonlinearities, while C_{ds} is fixed and equal to 34.7 fF.

$$C_{gs} = C_{gs\_min} + C_{gs0}(1 + \tanh(P_{10} + P_{11}V_{gs} + P_{111}V_{ds})) \cdot$$
$$\cdot (1 + \tanh(P_{20} + P_{21}V_{ds})) \tag{1}$$

$$C_{gd} = C_{gd\_min} + C_{gd0}(1 - P_{111} + \tanh(P_{30} - P_{31}V_{ds} + P_{1cc}V_{ds}V_{gs})) \cdot$$
$$\cdot (1 + \tanh(P_{40} + P_{41}(V_{gs} - V_{ds}) - P_{111}V_{ds}) + 2P_{111}) \qquad (2)$$

Firstly, the parasitic elements were extracted and de-embedded. Then, we carried out an initial estimation of the model parameters, followed by a numerical optimization to obtain the best fitting between measured and simulated capacitances (Fig. 1). Within this step, we used the calibrated S-parameters without uncertainty and obtained the nominal values of the model parameters, which are reported in Table II.

TABLE I
TRL CALIBRATION STANDARDS WITH ASSOCIATED STANDARD UNCERTAINTY: L AND W ARE THE LENGTH AND WIDTH OF THE LINES, H IS THE SUBSTRATE THICKNESS, AND SIGMA IS THE METAL CONDUCTIVITY.

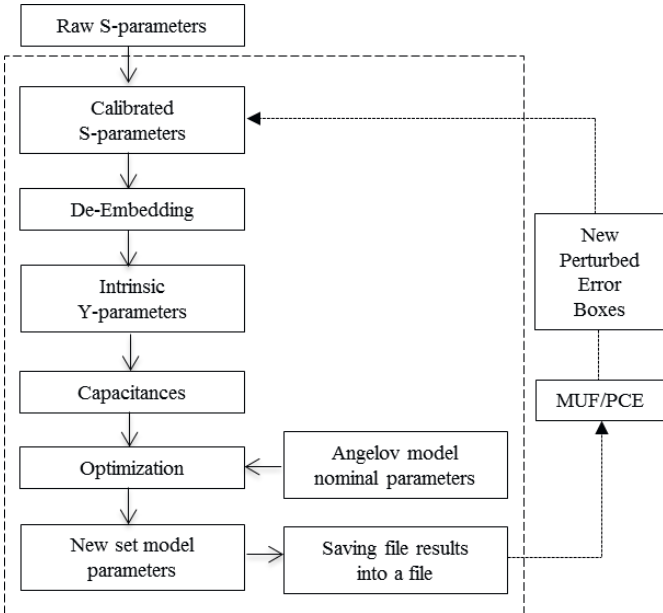|	L (μm)	W (μm)	H (μm)	σ (S)
Thru	570 ± 1.42	71 ± 1.42	100 ± 2.5	$2.05e7 \pm 0.10e7$
Line 1	2880 ± 1.42	71 ± 1.42	100 ± 2.5	$2.05e7 \pm 0.10e7$
Line 2	1490 ± 1.42	71 ± 1.42	100 ± 2.5	$2.05e7 \pm 0.10e7$
Line 3	980 ± 1.42	71 ± 1.42	100 ± 2.5	$2.05e7 \pm 0.10e7$
Short	285 ± 1.42	71 ± 1.42	100 ± 2.5	$2.05e7 \pm 0.10e7$

Fig. 1. Procedure for the identification of the capacitive-core model without the propagation of uncertainty (inside the dashed rectangle) and taking into account measurement uncertainty including MUF and PCE to generate perturbed error boxes.

B. Uncertainty analysis with PCE method

In the past years, PCE-based approaches were proposed in various engineering domains, such as in electronic and electromagnetic systems [5], [6]. However, to the authors' best knowledge, this is the first paper proposing the application of PCE to calibrate the uncertainty in measurement based transistor models, in order to reduce the computational time and resources required. The PCE model offers a strong mathematical rigorousness, giving an analytical representation of the statistical quantities of the system response: it is a stochastic expansion technique approximating a stochastic process by a series of orthogonal polynomials (also called basis functions) $\varphi_i(\xi)$ and suitable coefficients α_i. In the following, the variables ξ are assumed independent, but PCE can be also applied in case of correlated ones. The PCE basis functions depend on the distribution of the input random variables ξ and, for specific distributions (i.e., Gaussian, Uniform, Beta), are the polynomials of the Wiener-Askey scheme [1].With the PCE, a stochastic process **Y** is expressed as:

$$\mathbf{Y} = \sum_{i=0}^{\infty} \alpha_i \varphi_i(\xi) \cong \sum_{i=0}^{M} \alpha_i \varphi_i(\xi) \qquad (3)$$

In practice, this infinite PCE series is truncated to a certain number of bases functions M (*a priori:* depending on the maximum degree of the polynomials) such that the system response is accurately approximated [1], [5], [6].

Moreover, a recently proposed least angle regression selection (LARS) [2] approach allows computing a sparse PCE model: only those elements in (3) which have the largest influence on the PCE model response are selected.

In this Section, we consider the procedure described in Fig. 1. While in the literature simplified models have been used to accelerate optimization or parameter estimation [7], the aim of the proposed study is to quantify the uncertainty in the parameters of the capacitive core, as extracted from the Angelov model, by considering as sources of uncertainty fourteen independent Gaussian random variables related to the geometrical and physical characteristics of the calibration standards, as reported in Table I.

TABLE II
NOMINAL VALUE AND STANDARD UNCERTAINTY OBTAINED BY SENSITIVITY ANALYSIS (SA), MONTE CARLO (MC) ANALYSIS AND PCE METHOD

Parameters	Nominal	SA	MC	PCE
C_{gs_min}	53.00 fF	0.87 fF	0.91 fF	0.90 fF
C_{gs0}	49.62 fF	0.94 fF	0.93 fF	0.92 fF
C_{gd_min}	25.24 fF	0.46 fF	0.44 fF	0.46 fF
C_{gd0}	28.22 fF	0.57 fF	0.56 fF	0.61fF
P_{40}	4.001	0.006	0.005	0.005
$P_{11}=P_{41}$	5.225	0.003	0.002	0.002
P_{111}	0.2315	0.0001	0.0001	0.0001
P_{21}	0.309	0.001	0.001	0.001
P_{20}	-0.404	0.001	0.001	0.001
P_{10}	3.866	0.001	0.001	0.001
P_{30}	0.816	0.003	0.002	0.002
P_{31}	0.971	0.002	0.002	0.001
P_{1cc}	-0.552	0.001	0.001	0.001

Thirty samples for the chosen random variables are generated by a Latin Hypercube Sampling technique and a corresponding Angelov model is built following the identification procedure described in Fig. 1. Next, sparse PCE models based on polynomials with maximum degree 2 are computed via LARS for each one of the Angelov parameters shown in Table II. Using LARS, for example in case of C_{gd0}, results into only four number of terms in eq. 3, whereas a full PCE results into 120 elements. Note that stochastic moments can be obtained analytically from a PCE model [1], [2] and, if

needed, more complex stochastic functions such as the probability density (PDF) or the cumulative distribution (CDF) functions [5], [6].

In Table II we report the standard deviation of the Angelov model parameters obtained analytically from the PCE models.

III. RESULTS AND DISCUSSIONS

In order to validate the proposed approach, we compared uncertainty obtained by PCE method to the sensitivity and MC analyses results obtained by the NIST MUF [8]. To this aim, we used the NIST MUF in combination with a commercial circuit simulator and we propagated the uncertainty through the model parameters extraction procedure as illustrated in Fig. 1. Standard uncertainty obtained by PCE and NIST MUF are in good agreement, as shown in Table II.

Furthermore, in order to determine the (global) sensitivity of the Angelov model parameters with respect to the chosen random variables, PCE based SOBOL analysis is carried out and compared with the sensitivity analysis results: for example, the SOBOL indices obtained for C_{gd0} by means of the PCE show a similar trend compared to the sensitivity analysis results reported in Fig. 2. The latter shows the contribution of the error mechanisms related to the calibration process. The highest contribution is given by substrate thickness and width for the line 1 which is also the longest line used in the calibration process.

Finally, we propagated uncertainty in the capacitive-core model to the simulated FET output temporal voltages and currents. PCE models are computed for the voltage and current at the transistor ports by means of the LARS algorithm, starting from the port signals values obtained for the 30 Angelov models computed so far.

In Fig. 3, the standard deviation of the extrinsic drain current obtained analytically by means of the corresponding PCE model is compared to the corresponding value obtained by means of the MC analysis, showing a good agreement between the two methods. Similar results can be obtained for the other port waveforms.
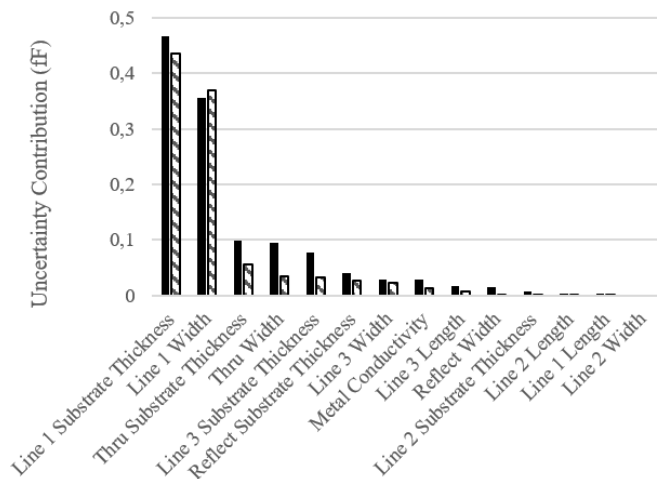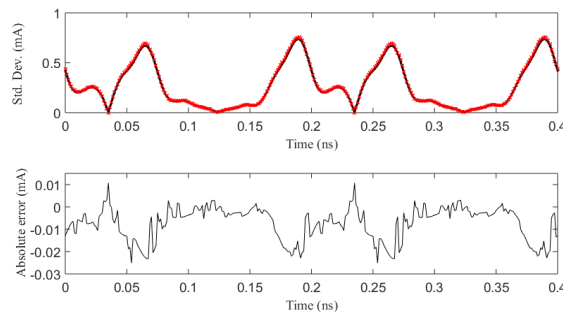

Fig. 3. Top: Standard deviation of the extrinsic drain current estimated by MC analysis (black continuous line) and PCE (red symbols). Bottom: Corresponding absolute error.

IV. CONCLUSIONS

We propagated measurement uncertainty to a transistor model with a novel approach based on the PCE method. To validate the proposed approach, a comparison with the uncertainty analysis performed with the NIST Microwave Uncertainty Framework is shown. Specifically, we showed that the novel method provides results in good agreement with the classical Monte-Carlo method but with a significant reduction of the computational effort. Furthermore, PCE is able to achieve successful results with thirty simulations compared to the one hundred required for MC. It is also important to note that, for application with a major number of error mechanisms, one hundred MC will be not enough to get representative results. Increasing the number of MC, it will require huge computational resources and it will be more time consuming.

The achieved results can contribute to give a new important insight to uncertainty quantification in measurement-based transistor models by considering the growing interest in uncertainty quantification in the field of microwave measurements.

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Fig. 2. Error contributions for PCE based SOBOL analysis (solid bars) and sensitivity analysis (diagonal filled bars).